

## Department of Electronics and Communication Engineering

## JUIT Waknaghat

A meeting of the Board of Studies of the Department of Electronics and Communication Engineering was held on 12.05.2017 at 10:05 AM in the Board Room.

The following members were present

- |                               |                 |
|-------------------------------|-----------------|
| 1. Prof. Sunil Vidya Bhooshan | Chairman        |
| 2. Prof. D.T. Shahani         | External Member |
| 3. Prof. R.C. Jain            | External Member |
| 4. Dr. Ghanshyam Singh        | Member          |
| 5. Dr. Rajiv Kumar            | Member          |
| 6. Dr. Shruti Jain            | Member          |
| 7. Dr. Neeru Sharma           | Member          |
| 8. Dr. Meenakshi Sood         | Member          |

The board approved the following suggestions:

1. The minutes of the last Board of Studies of the Department of Electronics and Communication Engineering which was held on 29.09.2016 at 11:05 AM in the Board Room was approved.
2. The new electives were approved for B-Tech and M-Tech

**B-Tech Electives :**

| S.No | Subject                                      |
|------|--|
| 1    | Applied Medical signal Processing            |
| 2    | Robotic Systems and Control                  |
| 3    | Time Frequency analysis and its applications |

**M-Tech Electives :**

| S.No | Subject                                     |
|------|---|
| 1    | VLSI in Biomedical Processing System        |
| 2    | Computational Intelligence and Applications |

PhD students can also opt for M-Tech electives also.

3. Syllabus of all the electives was approved. Find Annexure I.
4. The experiments of Analogue Electronics laboratory was approved as there is change in the syllabi of Session 2016-2020.

**Subject: Analogue Electronics Lab**

**Branch: ECE**

**Subject code: 10B17EC372**

**Semester: 3<sup>rd</sup>**

---

---

**List of Experiments**

1. To compare the performance of fixed bias circuit and emitter stabilized bias circuit and Voltage divider bias circuit.
2. To plot the drain and transfer characteristics of a JFET in common source configuration.
3. To design single stage CE amplifier using BJT and calculate the  $h$ -parameter model.
4. To investigate the effect of  $R_2$  and  $R_E$  on the stability of operating point for voltage divider bias circuit.
5. To design a RC coupled amplifier and observe frequency response.
6. To plot the frequency response of RC Coupled amplifier for different values of  $R_E$ .
7. To plot the frequency response of RC Coupled amplifier for different values of  $C_E$ .
8. Design two stage RC coupled amplifier
9. To study the performance of Darlington Pair Circuit
10. To observe the effect of negative feedback on the performance of the amplifier.

The meeting concluded with a vote of thanks by **Prof. Sunil Vidya Bhooshan**, Chairman, BOS, ECED.